VHDL:Modular Design and Synthesis of Cores and Systems, Third Edition

Zainalabedin Navabi

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VHDL:Modular Design and Synthesis of Cores and Systems, Third Edition Zainalabedin Navabi Utilize the Latest VHDL Tools and Techniques for Desigining Embedded Cores, Cutting-Edge Processors, RT Level Components, and Complex Digital Systems

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The cutting-edge resource explores the design of RT level components, the application of these components in a core-based, and the development of a complete processor design with its hardware and software as a core in a system-on-a-chip(SOC). Filled with over 150 illustrations, VHDL:Modular Design and Synthesis of Cores and Systems features:

An entire toolkit for register-transfer level digital system design

Testbench development techniques

New to this edition: Coverage of the latest uses of VHDL for digital system design, design of IP cores, interactive and self-checking testbench development, and VHDL's new libraries and packages

Inside this State-of-the-Art VHDL Design Tool

Design Methodology

VHDL Overview

Structure of VHDL

Simulation Model

Combinational Circuits

Sequential Circuits

Testbench Development

Control-Data Partitioned Designs

Design of RTL Embedded Cores

CPU RT Level Design

CPU Memory Indtruction Level Testing

Software Tools

Embedded System Design

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